

NOTES:  
 1. MATERIAL:  
 MOLDING: LCP UL94 V-0  
 CONTACT: COPPER ALLOY.  
 GOLD PLATED 30u" Min ON CONTACT AREA, 100u" Min TIN (LEAD FREE) ON SOLDER AREA,  
 50u" NICKEL PLATING OVER ALL.  
 SHIELD: SUS304-H, T=0.12±0.03mm

2. MECHANICAL:  
 INSERTION: 5~20N.  
 EXTRACTION: 8~20N.  
 DURABILITY: 10000 CYCLES

3. ELECTRICAL:  
 CURRENT: 5A MIN FOR VBUS;  
 0.25A MIN FOR OTHER.

VOLTAGE: 5VAC MAX  
 WITHSTANDING VOLTAGE: 100V AC.  
 CONTACT RESISTANCE: 40mΩ MAX.  
 INSULATION RESISTANCE: 100MΩ MIN.  
 4. ENVIRONMENTAL  
 TEMPERATURE RANGE -55°C ~ +85°C

FUC - R - 24 8 X - 10 - X - X  
 U3B C TYPE  
 R: Receptacle  
 PIN NO: 04-24

Series No. 包装:  
 T: TARY(盒子)  
 U: TUBE(管)  
 R: TAPE&REEL(卷)  
 颜色:  
 B: BLACK  
 N: NATURE

K: GOLD FLASH PLATING ON CONTACT AREA  
 Matte Tin PLATING ON SOLDER AREA  
 M: 5u" GOLD PLATING ON CONTACT AREA  
 Matte Tin PLATING ON SOLDER AREA  
 P: 15u" GOLD PLATING ON CONTACT AREA  
 Matte Tin PLATING ON SOLDER AREA  
 U: 30u" GOLD PLATING ON CONTACT AREA  
 Matte Tin PLATING ON SOLDER AREA

ROHS Compliant

TOLERANCE UNSPECIFIED	
±0.50	
±0.38	
±0.25	
±0.10	
±3.00°	
MANUFACTURING COPY	

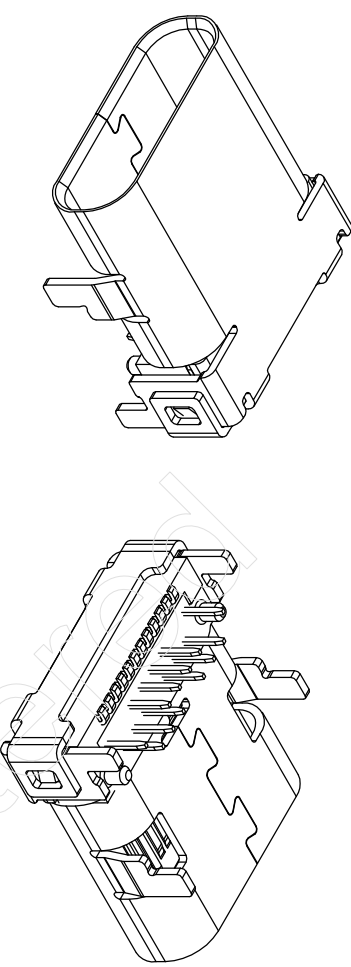


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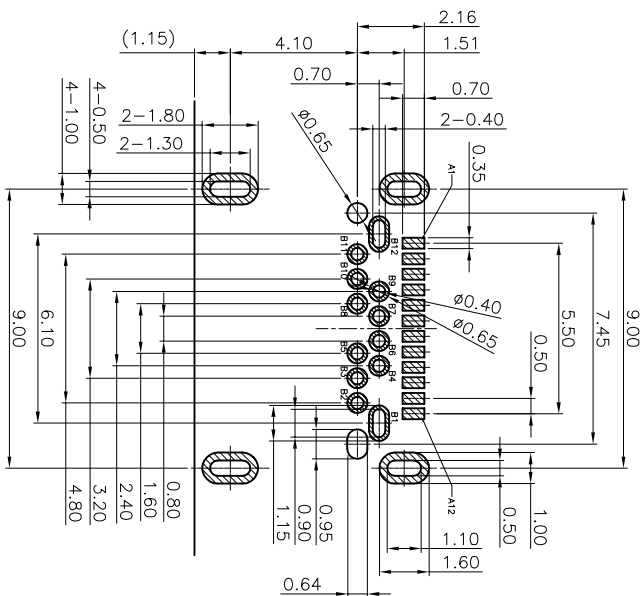
TITLE: USB 3.1 C TYPE FEMALE TOP CONNECTOR

DRAIN BY:	SIZE: A4	UNIT: mm.	PART NO. XNT-FUCR-248X-10XX
CHECKED BY:	SCALE: 1:1	PROJECTION	REV.: XA
APPROVED BY:	DWG. PATH		SHEET: 1/4

USB TYPE-C FULL-FEATURED RECEPTACLE INTERFACE PIN ASSIGNMENTS



PIN	Signal Name	Description	PIN	Signal Name	Description
A1	GND	Ground return	B12	GND	Ground return
A2	SSTRXp1	Positive half of first SuperSpeed TX differential pair	B11	SSRXn1	Positive half of first SuperSpeed RX differential pair
A3	SSTRXn1	Negative half of first SuperSpeed TX differential pair	B10	SSRXn1	Negative half of first SuperSpeed RX differential pair
A4	Vbus	Bus Power	B9	Vbus	Bus Power
A5	CC1	Configuration Channel	B8	SBU2	Sideband Use (SBU)
A6	Dp1	Positive half of the USB 2.0 differential pair-Position 1	B7	Dn2	Negative half of the USB 2.0 differential pair-Position 2
A7	Dn1	Negative half of the USB 2.0 differential pair-Position 1	B6	Dp2	Positive half of the USB 2.0 differential pair-Position 2
A8	SBU1	Sideband Use (SBU)	B5	CC2	Configuration Channel
A9	Vbus	Bus Power	B4	Vbus	Bus Power
A10	SSRXn2	Negative half of second SuperSpeed RX differential pair	B3	SSTRXn2	Negative half of second SuperSpeed TX differential pair
A11	SSRXp2	Positive half of second SuperSpeed RX differential pair	B2	SSTRXp2	Positive half of second SuperSpeed TX differential pair
A12	GND	Ground return	B1	GND	Ground return



TOLERANCE UNSPECIFIED	±0.50
.0	±0.38
.00	±0.25
.000	±0.10
ANGLE	±3.00°
MANUFACTURING COPY	



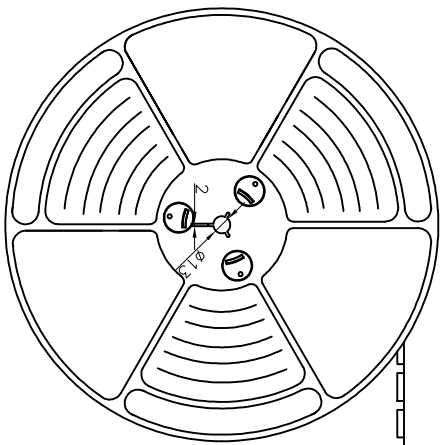
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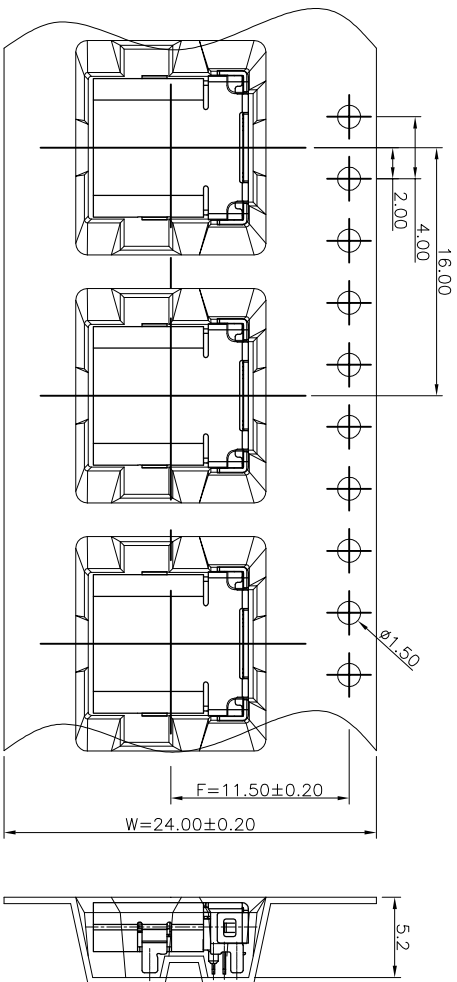
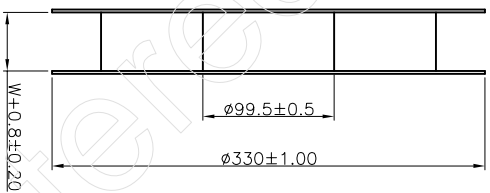
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APPROVED BY:	DWG. PATH		SHEET:2/4

RECOMMEND P.C.B LAYOUT( COMPONENT SIDE)  
TOLERANCE FOR PCB LAYOUT IS ±0.05  
KEEP OUT AREA

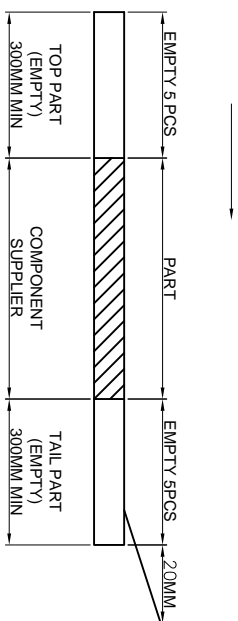


PULL OUT DIRECTION

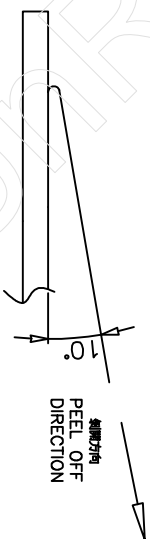


NOTES

1. 拔詢 擇僅  
LEAD TAPE LENGTH  
堤顯源研  
PULL OUT DIRECTION



2. 拔詢 擇僅 10.1~1.3N(10.2gf~130gf)  
PEELING OFF FORCE OF TOP TAPE:0.1~1.3N(10.2gf~130gf)  
堤顯源研 擇僅 擇僅 擇僅 擇僅 擇僅  
THIS REQUIREMENT SHOULD BE APPLIED AT SHIPMENT  
擇僅 擇僅 擇僅 擇僅 擇僅  
PEELING OFF SHOULD NOT BE ALLOWED DURING TRANSPORTATION



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